

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:
HENRY CHUNG

Docket: 30-4718 (4780)

Serial Number: 09/328,645

Group Art Unit: 2813

Filed: June 9, 1999

Examiner: Charles L. Bowers, Jr.

For: A FABRICATION METHOD OF INTEGRATED CIRCUITS WITH
BORDERLESS VIAS AND LOW DIELECTRIC-CONSTANT INTER-METAL
DIELECTRICS

6/Elc
11-2-99
J. Pratt

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, DC 20231

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Sir:

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In response to the Office Action mailed October 8, 1999, applicant makes the following election in response to the restriction requirement.

REMARKS

The Examiner has subjected this application to restriction under 35 U.S.C. 121. The Examiner has formed two groups of claims, the first drawn to an integrated circuit structure and the second drawn to a process for producing that structure. The Examiner has asserted that these groups of claims represent distinct inventions and may properly be restricted. Applicants hereby provisionally elect claim Group I directed to claims 1-8 for examination. However, the restriction requirement is traversed. It should be noted, the Commissioner may statutorily require the election of inventions "If two or more independent and distinct inventions are claimed in one application." In the instant case the Examiner is alleging that the inventions of groups one and two are distinct, although absolutely no showing of such distinctness has been made.

FAX COVER SHEET

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KINDLY DIRECT THIS COMMUNICATION TO:

EXAMINER : Charles L. Bowers, Jr.
GROUP : 2813

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